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1 Technical columns: ACM SIGACT news distributed computing column 5

Sergio Rajsbaum

December 2001 **ACM SIGACT News**, Volume 32 Issue 4

Full text available: pdf(1.77 MB) Additional Information: full citation, abstract, references, citings

The Distributed Computing Column covers the theory of systems that are composed of a number of interacting computing elements. These include problems of communication and networking, databases, distributed shared memory, multiprocessor architectures, operating systems, verification internet, and the web.This issue consists of four parts:• a survey of SIROCCO'01 by Pierre Fraigniaud,• a survey of POMC'01 by Rui Fan,• a survey of PODC'01 by myself,• the paper "Paxos Made Simple ...

2 An efficient architecture model for systematic design of application-specific multiprocessor SoC

A. Baghdadi, D. Lyonnard, N. Zergainoh, A. Jerraya

March 2001 **Proceedings of the conference on Design, automation and test in Europe**

Full text available: pdf(314.87 KB) Additional Information: full citation, references, citings, index terms

3 Classification Categories and Historical Development of Circuit Switching Topologies

George Broomell, J. Robert Heath

June 1983 **ACM Computing Surveys (CSUR)**, Volume 15 Issue 2

Full text available: pdf(2.55 MB) Additional Information: full citation, references, citings

4 IS '97: model curriculum and guidelines for undergraduate degree programs in information systems

Gordon B. Davis, John T. Gorgone, J. Daniel Couger, David L. Feinstein, Herbert E. Longenecker

December 1997 **ACM SIGMIS Database , Guidelines for undergraduate degree programs on Model curriculum and guidelines for undergraduate degree programs in information systems**, Volume 28 Issue 1

Full text available: pdf(7.24 MB) Additional Information: full citation, citings

5 Fast detection of communication patterns in distributed executions

Thomas Kunz, Michiel F. H. Seuren

November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**

Full text available: pdf(4.21 MB) Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

6 Computer utilities and the ESS: Accommodations or intimidations

Michael A. Duggan

January 1968 **Proceedings of the 1968 23rd ACM national conference**

Full text available: pdf(959.28 KB) Additional Information: full citation, abstract, references, index terms

Substantial problems are posed in the "computer utility" industry, particularly concerning possible large scale entry by communications common carriers. On the other hand, considerable excitement has been generated by the "computer utility" concept. By 1970 practically all computers will be capable of real-time operation. Most computers will be on-line (tied into other computers for direct communication). Some 60% of all computers will be tied into the nation's commu ...

7 A history of the Promis technology: an effective human interface

Jan Schultz

January 1986 **Proceedings of the ACM Conference on The history of personal workstations**

Full text available: pdf(2.61 MB) Additional Information: full citation, abstract, references, index terms

Scientific computing systems for individuals were pioneered early at Hewlett-Packard, beginning with the 9100A Desktop Calculator in 1968. Extensions of this first machine were soon seen in Personal Peripherals, such as Printers, Tape Cartridges, and Plotters, and followed by Graphic CRT Displays. By early 1972, the Desktop unit had been augmented by a very powerful Pocket Calculator, the ground-breaking HP 35A.This paper traces the evolution of these machines to the present day, ...

8 An information manipulation environment for monitoring parallel programs

Manfred Tscheligi, Sabine Musil

June 1994 **Proceedings of the workshop on Advanced visual interfaces**

Full text available: pdf(394.85 KB) Additional Information: full citation, abstract, references, index terms

Information Manipulation Environments (IME) are metaphor based visual environments for the problem oriented presentation and manipulation of non standard application functions. An IME is also characterized by excessive usage of direct manipulative interaction techniques. Based on these features we present INHOUSE, which is an information manipulation environment for user oriented monitoring of parallel system behavior. The functionality of the system is shown by a small and consistent set o ...

9 Measuring ISP topologies with rocketfuel

Neil Spring, Ratul Mahajan, David Wetherall, Thomas Anderson

February 2004 **IEEE/ACM Transactions on Networking (TON)**, Volume 12 Issue 1

Full text available: Additional Information:

pdf(732.86 KB) full citation, abstract, references, index terms

To date, realistic ISP topologies have not been accessible to the research community, leaving work that depends on topology on an uncertain footing. In this paper, we present new Internet mapping techniques that have enabled us to measure router-level ISP topologies. Our techniques reduce the number of required traces compared to a brute-force, all-to-all approach by three orders of magnitude without a significant loss in accuracy. They include the use of BGP routing tables to focus the measurem ...

Keywords: communication system operations and management, internet, measurement, network reliability

10 Scalable and flexible cosimulation of SoC designs with heterogeneous multi-processor target architectures

Patrice Gerin, Sungjoo Yoo, Gabriela Nicolescu, Ahmed A. Jerraya

January 2001 **Proceedings of the 2001 conference on Asia South Pacific design automation**

Full text available: pdf(214.44 KB) Additional Information: full citation, abstract, references, citings, index terms

In this paper, we present a cosimulation environment that provides modularity, scalability, and flexibility in cosimulation of SoC designs with heterogeneous multi-processor target architectures. Our cosimulation environment is based on an object-oriented simulation environment, SystemC. Exploiting the object orientation in SystemC representation, we achieve modularity and scalability of cosimulation by developing modular cosimulation interfaces. The object orientation also enables mixed-le ...

11 Neuro-fuzzy applications: Active electronic mail

S. Karnouskos, A. Vasilakos

March 2002 **Proceedings of the 2002 ACM symposium on Applied computing**

Full text available: pdf(532.13 KB) Additional Information: full citation, abstract, references, index terms

Network infrastructures have evolved tremendously over the last years, offering new capabilities to the applications in higher levels. Email is a widely used communication tool that could benefit of an intelligent and active underlying network in order to support sophisticated services. We explore in this paper an infrastructure based on intelligent mobile agents and active networks, and point out how and where advanced features can be introduced to our current passive email platform in order to ...

Keywords: active networks, computational intelligence, email, intelligent mobile agents

12 Multiprocessor SoC: design strategies and programming models: Benchmark-based design strategies for single chip heterogeneous multiprocessors

JoAnn M. Paul, Donald E. Thomas, Alex Bobrek

September 2004 **Proceedings of the 2nd IEEE/ACM/IFIP international conference on Hardware/software codesign and system synthesis**

Full text available: pdf(180.41 KB) Additional Information: full citation, abstract, references, index terms

Single chip heterogeneous multiprocessors are arising to meet the computational demands of portable and handheld devices. These computing systems are not fully custom designs traditionally targeted by the Design Automation (DA) community, general purpose designs traditionally targeted by the Computer Architecture (CA) community, nor pure embedded designs traditionally targeted by the real-time (RT) community. An entirely new design philosophy will be needed for this hybrid class of computing. Th ...

Keywords: benchmarking, heterogeneous multiprocessing, scenario-oriented design, systems-on-chips (SoCs)

13 Posters: A wireless handheld multi-modal digital video library client system

Michael R. Lyu, Jerome Yen, Edward Yau, Sam Sze

November 2003 **Proceedings of the 5th ACM SIGMM international workshop on Multimedia information retrieval**

Full text available: pdf(814.79 KB) Additional Information: full citation, abstract, references, index terms

We developed technologies for transmitting video contents over wireless platforms, and encapsulated these video delivery and presentation technologies into a client system for accessing a multi-modal digital video library. The mobile access system, *iVIEW client*, provides a user interface that meets the challenge of rich multi-modal information presentation on wireless hand-held devices. An XML schema is employed to organize the multi-modal metadata for better data interoperability. Furthe ...

Keywords: XML, browser and interface on mobile devices, mobile applications, multi-modal content retrieval, multimedia information retrieval, multimedia management and support

14 Computer-mediated communication in collaborative educational settings (report of the ITiCSE '97 working group on CMC in collaborative educational settings)

Ursula Wolz, Jacob Palme, Penny Anderson, Zhi Chen, James Dunne, Göran Karlsson, Atika Laribi, Sirkku Männikkö, Robert Spielvogel, Henry Walker

June 1997 **The supplemental proceedings of the conference on Integrating technology into computer science education: working group reports and supplemental proceedings**

Full text available: pdf(109.30 KB) Additional Information: full citation, references, citings, index terms

15 Computer-mediated communication in collaborative educational settings: report of the ITiCSE '97 working group on CMC in collaborative educational settings

Ursula Wolz, Jacob Palme, Penny Anderson, Zhi Chen, James Dunne, Göran Karlsson, Atika Laribi, Sirkku Männikkö, Robert Spielvogel, Henry Walker

October 1997 **ACM SIGCUE Outlook**, Volume 25 Issue 4

Full text available: pdf(2.14 MB) Additional Information: full citation, abstract, references, index terms

In educational environments that stress collaboration, the use of computer-mediated communication (CMC) tools can be a source of support as well as a challenge. This paper begins by considering general educational and economic goals and how CMC can be helpful in attaining these goals. A taxonomy of tools for communication and collaboration in education is described. Many sides of the issue are considered, including the roles of teachers and students, problems that can arise and potential solutio ...

16 The state of the art in locally distributed Web-server systems

Valeria Cardellini, Emiliano Casalicchio, Michele Colajanni, Philip S. Yu

June 2002 **ACM Computing Surveys (CSUR)**, Volume 34 Issue 2

Full text available: pdf(1.41 MB) Additional Information: full citation, abstract, references, citings, index terms

The overall increase in traffic on the World Wide Web is augmenting user-perceived response times from popular Web sites, especially in conjunction with special events. System platforms that do not replicate information content cannot provide the needed

scalability to handle large traffic volumes and to match rapid and dramatic changes in the number of clients. The need to improve the performance of Web-based services has produced a variety of novel content delivery architectures. This article w ...

Keywords: Client/server, World Wide Web, cluster-based architectures, dispatching algorithms, distributed systems, load balancing, routing mechanisms

17 An internetwork memo distribution capability—MMDF

David H. Crocker, Edward S. Szurkowski, David J. Farber

November 1979 **Proceedings of the sixth symposium on Data communications**

Full text available: pdf(710.41 KB) Additional Information: full citation, abstract, references, citings, index terms

The advent of packet-switched networks has led to increased use of computers for sending text messages (memos) between people. However, attachment to common carrier or equivalent packet networks is expensive and/or restricted by organizational policies. In addition, use of several networks creates the need for relaying memos between them. The work described here is designed to free users from dependence upon any particular communication environment and to provide a memo distribution facilit ...

18 Computer networking for large computers in Universities

J. Matsukata

August 1987 **ACM SIGCOMM Computer Communication Review , Proceedings of the ACM workshop on Frontiers in computer communications technology**, Volume 17 Issue 5

Full text available: pdf(1.06 MB) Additional Information: full citation, abstract, references, index terms

N-1 protocol was developed for construction of a computer network which connects universities in Japan. N-1 protocol has been adopted by Inter-University Computer Network and University Library Network. NACSIS has started the construction of Science Information Network which will provide an infrastructure for digital communication for universities in Japan. High speed digital lines has been leased from NTT for that purpose. A local area network was constructed at the F ...

19 OceanStore: an architecture for global-scale persistent storage

John Kubiatowicz, David Bindel, Yan Chen, Steven Czerwinski, Patrick Eaton, Dennis Geels, Ramakrishna Gummadi, Sean Rhea, Hakim Weatherspoon, Chris Wells, Ben Zhao

November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 28 , 34 Issue 5 , 5

Full text available: pdf(166.53 KB) Additional Information: full citation, abstract, references, citings, index terms

OceanStore is a utility infrastructure designed to span the globe and provide continuous access to persistent information. Since this infrastructure is comprised of untrusted servers, data is protected through redundancy and cryptographic techniques. To improve performance, data is allowed to be cached anywhere, anytime. Additionally, monitoring of usage patterns allows adaptation to regional outages and denial of service attacks; monitoring also enhances performance through pro-active movement ...

20 OceanStore: an architecture for global-scale persistent storage

John Kubiatowicz, David Bindel, Yan Chen, Steven Czerwinski, Patrick Eaton, Dennis Geels, Ramakrishan Gummadi, Sean Rhea, Hakim Weatherspoon, Westley Weimer, Chris Wells, Ben Zhao

November 2000 **ACM SIGPLAN Notices**, Volume 35 Issue 11

Full text available: pdf(1.47 MB) Additional Information: full citation, abstract, references, index terms

OceanStore is a utility infrastructure designed to span the globe and provide continuous access to persistent information. Since this infrastructure is comprised of untrusted servers, data is protected through redundancy and cryptographic techniques. To improve performance, data is allowed to be cached anywhere, anytime. Additionally, monitoring of usage patterns allows adaptation to regional outages and denial of service attacks; monitoring also enhances performance through pro-active movement ...

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21 Automatic generation of application-specific architectures for heterogeneous multiprocessor system-on-chip

Damien Lyonnard, Sungjoo Yoo, Amer Baghdadi, Ahmed A. Jerraya

June 2001 **Proceedings of the 38th conference on Design automation**

Full text available: pdf(285.15 KB) Additional Information: full citation, abstract, references, citings, index terms

We present a design flow for the generation of application-specific multiprocessor architectures. In the flow, architectural parameters are first extracted from a high-level system specification. Parameters are used to instantiate architectural components, such as processors, memory modules and communication networks. The flow includes the automatic generation of communication coprocessor that adapts the processor to the communication network in an application-specific way. Experiments with ...

22 Predictability requirements of a soft modem

Michael B. Jones, Stefan Saroiu

June 2001 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2001 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 29 Issue 1

Full text available: pdf(1.53 MB) Additional Information: full citation, abstract, references, citings

Soft Modems use the main processor to execute modem functions traditionally performed by hardware on the modem card. To function correctly, soft modems require that ongoing signal processing computations be performed on the host CPU in a timely manner. Thus, signal processing is a commonly occurring background real-time application---one running on systems that were not designed to support predictable real-time execution. This paper presents a detailed study of the performance characteris ...

Keywords: CPU scheduling, Rialto, Rialto/NT, Windows 2000, Windows NT, open real-time system, real-time, signal processing, soft devices, soft modem

23 The Wisconsin multicube: a new large-scale cache-coherent multiprocessor

J. R. Goodman, P. J. Woest

May 1988 **ACM SIGARCH Computer Architecture News , Proceedings of the 15th Annual International Symposium on Computer architecture**, Volume 16 Issue 2

Full text available: Additional Information: full citation, abstract, references, citings, index

pdf(1.27 MB) terms

The Wisconsin Multicube, is a large-scale, shared-memory multiprocessor architecture that employs a snooping cache protocol over a grid of buses. Each processor has a conventional (SRAM) cache optimized to minimize memory latency and a large (DRAM) snooping cache optimized to reduce bus traffic and to maintain consistency. The large snooping cache should guarantee that nearly all the traffic on the buses will be generated by I/O and accesses to shared data. The p ...

24 MEDEA workshop: Fine-grain design space exploration for a cartographic SoC multiprocessor

Alessio Bechini, Pierfrancesco Foglia, Cosimo Antonio Prete
March 2003 **ACM SIGARCH Computer Architecture News**, Volume 31 Issue 1

Full text available: pdf(948.91 KB) Additional Information: full citation, abstract, references, index terms

Traditionally, in the field of embedded systems low power consumption and low cost have been always regarded as stringent specification constraints. In recent years, high computational power has become a fundamental requirement as well. This has been mainly determined by the introduction of new features, typical of general-purpose systems, e.g. GUI-based interfaces. In this setting, low cost, low power consumption, significant computational power and short time-to-market are conflicting needs th ...

Keywords: SoC Multiprocessors, embedded systems, multiprocessor architecture, performance evaluation, trace-driven simulation

25 Pen computing: a technology overview and a vision

André Meyer
July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

Full text available: pdf(5.14 MB) Additional Information: full citation, abstract, citings, index terms

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions.Starting with a short historic ...

26 Parallel program performance prediction using deterministic task graph analysis

Vikram S. Adve, Mary K. Vernon
February 2004 **ACM Transactions on Computer Systems (TOCS)**, Volume 22 Issue 1

Full text available: pdf(576.29 KB) Additional Information: full citation, abstract, references, index terms

In this article, we consider analytical techniques for predicting detailed performance characteristics of a single shared memory parallel program for a particular input. Analytical models for parallel programs have been successful at providing simple qualitative insights and bounds on program scalability, but have been less successful in practice for providing detailed insights and metrics for program performance (leaving these to measurement or simulation). We develop a conceptually simple mode ...

Keywords: Analytical model, deterministic model, parallel program performance prediction, queueing network, shared memory, task graph, task scheduling

27 A generic wrapper architecture for multi-processor SoC cosimulation and design

Sungjoo Yoo, Gabriela Nicolescu, Damien Lyonnard, Amer Baghdadi, Ahmed A. Jerraya

April 2001 **Proceedings of the ninth international symposium on Hardware/software codesign**

Full text available: pdf(721.32 KB) Additional Information: full citation, abstract, references, citings, index terms

In communication refinement with multiple communication protocols and abstraction levels, the system specification is described by heterogeneous components in terms of communication protocols and abstraction levels. To adapt each heterogeneous component to the other part of system, we present a generic wrapper architecture that can adapt different protocols or different abstraction levels, or both. In this paper, we give a detailed explanation of applying the generic wrapper architecture to m ...

28 Simultaneous multithreading: maximizing on-chip parallelism

Dean M. Tullsen, Susan J. Eggers, Henry M. Levy

May 1995 **ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual international symposium on Computer architecture**, Volume 23 Issue 2

Full text available: pdf(1.35 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper examines *simultaneous multithreading,* a technique permitting several independent threads to issue instructions to a superscalar's multiple functional units in a single cycle. We present several models of simultaneous multithreading and compare them with alternative organizations: a wide superscalar, a fine-grain multithreaded processor, and single-chip, multiple-issue multiprocessing architectures. Our results show that both (single-threaded) superscalar and fine-grain multithr ...

29 Simultaneous multithreading: maximizing on-chip parallelism

Dean M. Tullsen, Susan J. Eggers, Henry M. Levy

August 1998 **25 years of the international symposia on Computer architecture (selected papers)**

Full text available: pdf(1.48 MB) Additional Information: full citation, references, citings, index terms

30 SPLASH: Stanford parallel applications for shared-memory

Jaswinder Pal Singh, Wolf-Dietrich Weber, Anoop Gupta

March 1992 **ACM SIGARCH Computer Architecture News**, Volume 20 Issue 1

Full text available: pdf(3.04 MB) Additional Information: full citation, abstract, citings, index terms

We present the Stanford Parallel Applications for Shared-Memory (SPLASH), a set of parallel applications for use in the design and evaluation of shared-memory multiprocessing systems. Our goal is to provide a suite of realistic applications that will serve as a well-documented and consistent basis for evaluation studies. We describe the applications currently in the suite in detail, discuss some of their important characteristics, and explore their behavior by running them on a real multiprocess ...

31 Performance improvement of multi-processor systems cosimulation based on SW analysis

J. Jung, S. Yoo, K. Choi

March 2001 **Proceedings of the conference on Design, automation and test in Europe**

Full text available: pdf(154.49 KB) Additional Information: full citation, references, index terms

32 Communication and consistency protocols: Detailed cache coherence characterization for OpenMP benchmarks

Jaydeep Marathe, Anita Nagarajan, Frank Mueller
June 2004 **Proceedings of the 18th annual international conference on Supercomputing**
Full text available: pdf(358.00 KB) Additional Information: full citation, abstract, references, index terms

Past work on studying cache coherence in shared-memory symmetric multiprocessors (SMPs) concentrates on studying aggregate events, often from an architecture point of view. However, this approach provides insufficient information about the exact sources of inefficiencies in parallel applications. For SMPs in contemporary clusters, application performance is impacted by the pattern of shared memory usage, and it becomes essential to understand coherence behavior in terms of the application progra ...

Keywords: SMPs, cache analysis, coherence protocols, dynamic binary rewriting, program instrumentation

33 Low power DSP's for wireless communications (embedded tutorial session)
Ingrid Verbauwhede, Chris Nicol
August 2000 **Proceedings of the 2000 international symposium on Low power electronics and design**
Full text available: pdf(424.32 KB) Additional Information: full citation, abstract, references, citings, index terms

Wireless communications and more specifically, the fast growing penetration of cellular phones and cellular infrastructure are the major drivers for the development of new programmable Digital Signal Processors (DSPs). In this tutorial, an overview will be given of recent developments in DSP processor architectures, that makes them well suited to execute computationally intensive algorithms typically found in communications systems. DSP processors have adapted instruction sets, memory archi ...

Keywords: architectures, digital signal processing, programmable processors, wireless communications

34 Highly available systems for database applications
Won Kim
March 1984 **ACM Computing Surveys (CSUR)**, Volume 16 Issue 1
Full text available: pdf(2.43 MB) Additional Information: full citation, abstract, references, citings, index terms, review

As users entrust more and more of their applications to computer systems, the need for systems that are continuously operational (24 hours per day) has become even greater. This paper presents a survey and analysis of representative architectures and techniques that have been developed for constructing highly available systems for database applications. It then proposes a design of a distributed software subsystem that can serve as a unified framework for constructing database applica ...

35 Using high performance GIS software to visualize data: a hands-on software demonstration
Linda Burton, William Hatchett, Mari Hobkirk, Charles Powell
November 1998 **Proceedings of the 1998 ACM/IEEE conference on Supercomputing (CDROM)**
Full text available: html(80.49 KB) Additional Information: full citation, abstract, references

Since 1995 Wheat Ridge High School (WRHS) students have participated in a mapping project involving local open space, in conjunction with NASA. Students have learned to use *Idrisi,* a Geographical Imaging Systems (GIS) software, as well as other GIS programs *Arc*

View and *Multispec,* to plan the location of a trail along Colorado's front range.As this project has progressed, students have learned the GIS technology as well as many science issues related to trail mapping. Simila ...

36 Object distribution in Orca using Compile-Time and Run-Time techniques

Henri E. Bal, M. Frans Kaashoek

October 1993 **ACM SIGPLAN Notices , Proceedings of the eighth annual conference on Object-oriented programming systems, languages, and applications**, Volume 28 Issue 10

Full text available: pdf(1.70 MB) Additional Information: full citation, references, citings, index terms

37 WiPPET, a virtual testbed for parallel simulations of wireless networks

Jignesh Panchal, Owen Kelly, Jie Lai, Narayan Mandayam, Andrew T. Ogielski, Roy Yates

July 1998 **ACM SIGSIM Simulation Digest , Proceedings of the twelfth workshop on Parallel and distributed simulation**, Volume 28 Issue 1

Full text available: pdf(971.79 KB) Publisher Site Additional Information: full citation, references, citings, index terms

38 Large-scale experimental study of Internet performance using video traffic

Dmitri Loguinov, Hayder Radha

January 2002 **ACM SIGCOMM Computer Communication Review**, Volume 32 Issue 1

Full text available: pdf(1.23 MB) Additional Information: full citation, abstract, references, citings, index terms

In this paper, we analyze the results of a seven-month real-time streaming experiment, which was conducted between a number of unicast dialup clients, connecting to the Internet through access points in more than 600 major U.S. cities, and a backbone video server. During the experiment, the clients streamed low-bitrate MPEG-4 video sequences from the server over paths with more than 5,000 distinct Internet routers. We describe the methodology of the experiment, the architecture of our NACK-based ...

39 High-bandwidth address translation for multiple-issue processors

Todd M. Austin, Gurindar S. Sohi

May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd annual international symposium on Computer architecture**, Volume 24 Issue 2

Full text available: pdf(1.56 MB) Additional Information: full citation, abstract, references, citings, index terms

In an effort to push the envelope of system performance, microprocessor designs are continually exploiting higher levels of instruction-level parallelism, resulting in increasing bandwidth demands on the address translation mechanism. Most current microprocessor designs meet this demand with a multi-ported TLB. While this design provides an excellent hit rate at each port, its access latency and area grow very quickly as the number of ports is increased. As bandwidth demands continue to increase ...

40 Using objects to design and build radar ESM systems

Brian M. Barry, John R. Altoft

December 1987 **ACM SIGPLAN Notices , Conference proceedings on Object-oriented programming systems, languages and applications**, Volume 22 Issue 12

Full text available: pdf(1.03 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the application of object-oriented programming to the design of a

multiprocessor ESM testbed. The ESM testbed uses an object-oriented development environment which integrates Smalltalk and C language tools with the Harmony real-time operating system in a shared memory multiprocessor. All development for an application is done using personal computers which are themselves processors in the real-time testbed. We first discuss two aspects of the ESM testbed: a framework fo ...

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41 Level II technical support in a distributed computing environment

Tim Leehane

September 1996 **Proceedings of the 24th annual ACM SIGUCCS conference on User services**

Full text available: pdf(5.73 MB) Additional Information: full citation, references, index terms

42 Automatic Generation of Fast Timed Simulation Models for Operating Systems in SoC Design

S. Yoo, G. Nicolescu, L. Gauthier, A. Jerraya

March 2002 **Proceedings of the conference on Design, automation and test in Europe**

Full text available: pdf(158.87 KB) Publisher Site Additional Information: full citation, abstract

To enable fast and accurate evaluation of HW/SW implementationchoices of on-chip communication, we presenta method to automatically generate timed OS simulationmodels. The method generates the OS simulation modelswith the simulation environment as a virtual processor.Since the generated OS simulation models use finalOS code, the presented method can mitigate the OS codeequivalence problem. The generated model also simulatesdifferent types of processor exceptions. This approach providestwo orders ...

43 A systematic approach to software peripherals for embedded systems

D. Lioupis, A. Papagiannis, D. Psihogiou

April 2001 **Proceedings of the ninth international symposium on Hardware/software codesign**

Full text available: pdf(562.90 KB) Additional Information: full citation, abstract, references, index terms

The continued growth of microprocessors' performance and the need for better CPU utilization, has led to the introduction of the software peripherals' approach: By this term we refer to software modules that can successfully emulate peripherals that, until now, were traditionally implemented in hardware. Software implementations offer great flexibility in product design and in functional upgrades, while they have high contribution in the cost/performance ratio optimization. We focus on embedd ...

Keywords: embedded processors, reconfigurable architectures, software peripherals

44 Continuous profiling: where have all the cycles gone?
Jennifer M. Anderson, Lance M. Berc, Jeffrey Dean, Sanjay Ghemawat, Monika R. Henzinger, Shun-Tak A. Leung, Richard L. Sites, Mark T. Vandevoorde, Carl A. Waldspurger, William E. Weihl
October 1997 **ACM SIGOPS Operating Systems Review , Proceedings of the sixteenth ACM symposium on Operating systems principles**, Volume 31 Issue 5
Full text available: pdf(2.29 MB) Additional Information: full citation, references, citings, index terms

45 A closer look at coscheduling approaches for a network of workstations
Shailabh Nagar, Ajit Banerjee, Anand Sivasubramaniam, Chita R. Das
June 1999 **Proceedings of the eleventh annual ACM symposium on Parallel algorithms and architectures**
Full text available: pdf(1.38 MB) Additional Information: full citation, references, citings, index terms

46 LogP: towards a realistic model of parallel computation
David Culler, Richard Karp, David Patterson, Abhijit Sahay, Klaus Erik Schauser, Eunice Santos, Ramesh Subramonian, Thorsten von Eicken
July 1993 **ACM SIGPLAN Notices , Proceedings of the fourth ACM SIGPLAN symposium on Principles and practice of parallel programming**, Volume 28 Issue 7
Full text available: pdf(1.51 MB) Additional Information: full citation, abstract, references, citings, index terms

A vast body of theoretical research has focused either on overly simplistic models of parallel computation, notably the PRAM, or overly specific models that have few representatives in the real world. Both kinds of models encourage exploitation of formal loopholes, rather than rewarding development of techniques that yield performance across a range of current and future parallel machines. This paper offers a new parallel machine model, called LogP, that reflects the critical technology tre ...

Keywords: PRAM, complexity analysis, massively parallel processors, parallel algorithms, parallel models

47 Video: Measurement study of low-bitrate internet video streaming
Dmitri Loguinov, Hayder Radha
November 2001 **Proceedings of the First ACM SIGCOMM Workshop on Internet Measurement**
Full text available: pdf(3.00 MB) Additional Information: full citation, abstract, references, citings, index terms

In this paper, we analyse the results of a seven-month real-time streaming experiment, which was conducted between a number of unicast dialup clients, connecting to the Internet through access points in more than 600 major U.S. cities, and a backbone video server. During the experiment, the clients streamed low-bitrate MPEG-4 video sequences from the server over paths with more than 5,000 distinct Internet routers. We describe the methodology of the experiment, the architecture of our NACK-based ...

48 Distributed operating systems
Andrew S. Tanenbaum, Robbert Van Renesse
December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4
Full text available: pdf(5.49 MB) Additional Information: full citation, abstract, references, citings, index

terms, review

Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

49 Computer Communication Networks: Approaches, Objectives, and Performance Considerations

Stephen R. Kimbleton, G. Michael Schneider

September 1975 **ACM Computing Surveys (CSUR)**, Volume 7 Issue 3

Full text available: pdf(3.99 MB) Additional Information: full citation, references, citings, index terms

50 Challenges: Challenges:: environmental design for pervasive computing systems

Ravi Jain, John Wullert

September 2002 **Proceedings of the 8th annual international conference on Mobile computing and networking**

Full text available: pdf(212.37 KB) Additional Information: full citation, abstract, references, index terms

We argue that pervasive computing offers not only tremendous opportunities and exciting research challenges but also possible negative environmental impacts, particularly in terms of physical waste and energy consumption. These environmental impacts will come under increasing government and consumer scrutiny, and like other disciplines (e.g. architecture, transportation), pervasive computing will have to adapt accordingly. Further, we argue that software-related issues will play an increasing ro ...

Keywords: environmental impacts, green computing, pervasive computing

51 A system-level specification framework for I/O architectures

Mark D. Hill, Anne E. Condon, Manoj Plakal, Daniel J. Sorin

June 1999 **Proceedings of the eleventh annual ACM symposium on Parallel algorithms and architectures**

Full text available: pdf(1.18 MB) Additional Information: full citation, references, citings, index terms

52 The sort-first rendering architecture for high-performance graphics

Carl Mueller

April 1995 **Proceedings of the 1995 symposium on Interactive 3D graphics**

Full text available: pdf(4.07 MB) Additional Information: full citation, abstract, references, citings, index terms

Interactive graphics applications have long been challenging graphics system designers by demanding machines that can provide ever increasing polygon rendering performance. Another trend in interactive graphics is the growing use of display devices with pixel counts well beyond what is usually considered "high-resolution." If we examine the architectural space of high-performance rendering systems, we discover only one architectural class that promises to deliver high polygon pe ...

53 A direct signaling system for flexible access and deployment of telecommunication services

Thomas F. La Porta, Kuo-Wei Herman Chen
August 1997 **IEEE/ACM Transactions on Networking (TON)**, Volume 5 Issue 4
Full text available: pdf(219.51 KB) Additional Information: full citation, references, index terms

Keywords: ISDN, intellignet networks, signaling

54 Exokernel: an operating system architecture for application-level resource management
D. R. Engler, M. F. Kaashoek, J. O'Toole
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5
Full text available: pdf(2.16 MB) Additional Information: full citation, references, citings, index terms

55 Insights into the implementation and application of heterogeneous local area networks
William P. Lidinsky
October 1981 **Proceedings of the seventh symposium on Data communications**
Full text available: pdf(822.70 KB) Additional Information: full citation, abstract, references, index terms

The ideal local area network is a mechanism which provides concurrent high speed error-free data paths over a limited geographical area and between any computational entities on the network. A computational entity may be a program running on any type of computer, any intelligent device, or any terminal. This means that such an ideal network must be able to support systems of cooperating processes within disjoint and dissimilar host environments. Achieving such an ideal heterogene ...

56 Efficient instruction cache simulation and execution profiling with a threaded-code interpreter
Peter S. Magnusson
December 1997 **Proceedings of the 29th conference on Winter simulation**
Full text available: pdf(912.22 KB) Additional Information: full citation, references, index terms

57 Design of an integrated services packet network
Jonathan S. Turner
September 1985 **ACM SIGCOMM Computer Communication Review , Proceedings of the ninth symposium on Data communications**, Volume 15 Issue 4
Full text available: pdf(1.13 MB) Additional Information: full citation, abstract, references, citings, index terms

The Integrated Services Digital Network (ISDN) has been proposed as a way of providing integrated voice and data communications services on a universal or near-universal basis. In this paper, I argue that the evolutionary approach inherent in current ISDN proposals is unlikely to provide an effective long term solution and advocate a more revolutionary approach, based on the use of advanced packet switching technology. The bulk of this paper is devoted to a detailed description of an Integr ...

58 A network environment for computer-supported cooperative work
J. Whitescarver, P. Mukherji, M. Turoff, R. J. DeBlock, R. M. Czech, B. K. Paul
August 1987 **ACM SIGCOMM Computer Communication Review , Proceedings of the ACM workshop on Frontiers in computer communications technology**,

Volume 17 Issue 5
Full text available: pdf(1.37 MB) Additional Information: full citation, abstract, references, index terms

A second generation computer supported cooperative work system called Electronic Information Exchange System (EIES2) is described in this paper. The EIES2 communications environment allows users to network across geographical constraints using asynchronous or synchronous communications. The architecture of the network environment is decentralized, and is implemented using modern standards, and an easy-to-use user interface. At the heart of EIES2 is a high-level, object oriented p ...

59 SWiMNet: a scalable parallel simulation testbed for wireless and mobile networks
Azzedine Boukerche, Sajal K. Das, Alessandro Fabbri
September 2001 **Wireless Networks**, Volume 7 Issue 5

Full text available: pdf(397.98 KB) Additional Information: full citation, abstract, references, index terms

We present a framework, called SWiMNet, for parallel simulation of wireless and mobile PCS networks, which allows realistic and detailed modeling of mobility, call traffic, and PCS network deployment. SWiMNet is based upon event precomputation and a combination of optimistic and conservative synchronization mechanisms. Event precomputation is the result of model independence within the global PCS network. Low percentage of blocked calls typical for PCS networks is exploited in the channel alloca ...

Keywords: PCS network models, framework for PCS network simulation, parallel discrete event simulation, performance analysis

60 Session 24: software tools: The D editor: a new interactive parallel programming tool
Seema Hiranandani, Ken Kennedy, Chau Wen Tseng, Scott Warren
November 1994 **Proceedings of the 1994 ACM/IEEE conference on Supercomputing**

Full text available: pdf(1.58 MB) Additional Information: full citation, abstract, references

Fortran D and High Performance Fortran are languages designed to support efficient data-parallel programming on a variety of parallel architectures. The goal of the D Editor is to provide a tool that allows scientists to use these languages efficiently. The D Editor combines analyses for shared-memory machines and compiler optimizations for distributed-memory machines. By cooperating with the underlying compiler, it can provide novel information on partitioning, parallelism, and communication ba ...

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Refine Search (multiprocessor or processors) and (modem or phone)

	PAT. NO.	Title
1	6,640,145	Media recording device with packet data interface
2	6,400,996	Adaptive pattern recognition based control system and method
3	5,920,477	Human factored interface incorporating adaptive pattern recognition based controller apparatus
4	5,875,108	Ergonomic man-machine interface incorporating adaptive pattern recognition based control system
5	5,774,357	Human factored interface incorporating adaptive pattern recognition based controller apparatus

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	PAT. NO.	Title
1	6,768,944	Method and system for controlling a vehicle
2	6,760,916	Method, system and computer program product for producing and distributing enhanced media downstreams
3	6,757,710	Object-based on-line transaction infrastructure
4	6,754,885	Methods and apparatus for controlling object appearance in a process control configuration system
5	6,754,181	System and method for a directory service supporting a hybrid communication system architecture
6	6,748,353	Authoring language translator
7	6,742,015	Base services patterns in a netcentric environment
8	6,731,625	System, method and article of manufacture for a call back architecture in a hybrid network with support for internet telephony
9	6,727,927	System, method and article of manufacture for a user interface for a knowledge management tool
10	6,725,209	Computerized medical diagnostic and treatment advice system and method including mental status examination
11	6,721,726	Knowledge management tool
12	6,721,713	Business alliance identification in a web architecture framework
13	6,720,920	Method and arrangement for communicating between vehicles
14	6,718,535	System, method and article of manufacture for an activity framework design in an e-commerce based environment
15	6,715,145	Processing pipeline in a base services pattern environment
16	6,707,812	System, method and article of manufacture for element management in a hybrid communication system
17	6,704,873	Secure gateway interconnection in an e-commerce based environment

18 6,704,303 IP/telephony user interface for a hybrid communication system
19 6,701,345 Providing a notification when a plurality of users are altering similar data in a health care solution environment
20 6,671,818 Problem isolation through translating and filtering events into a standard object format in a network based supply chain
21 6,662,357 Managing information in an integrated development architecture framework
22 6,650,869 System and method for managing return channel bandwidth in a two-way satellite system
23 6,643,640 Method for performing a data query
24 6,640,304 Systems and methods for secure transaction management and electronic rights protection
25 6,640,249 Presentation services patterns in a netcentric environment
26 6,640,244 Request batcher in a transaction services patterns environment
27 6,640,238 Activity component in a presentation services patterns environment
28 6,636,242 View configurer in a presentation services patterns environment
29 6,633,878 Initializing an ecommerce database framework
30 6,633,835 Prioritized data capture, classification and filtering in a network monitoring environment
31 6,629,081 Account settlement and financing in an e-commerce environment
32 6,615,253 Efficient server side data retrieval for execution of client side applications
33 6,615,199 Abstraction factory in a base services pattern environment
34 6,615,166 Prioritizing components of a network framework required for implementation of technology
35 6,611,867 System, method and article of manufacture for implementing a hybrid network
36 6,609,128 Codes table framework design in an E-commerce architecture
37 6,608,892 Automatic routing and information system for telephone services
38 6,606,744 Providing collaborative installation management in a network-based supply chain environment
39 6,606,660 Stream-based communication in a communication services patterns environment
40 6,601,234 Attribute dictionary in a business logic services environment
41 6,601,233 Business components framework
42 6,601,192 Assertion component in environment services patterns
43 6,581,008 Space weather prediction system and method
44 6,578,068 Load balancer in environment services patterns
45 6,578,056 Efficient data transfer mechanism for synchronization of multi-media databases
46 6,571,282 Block-based communication in a communication services patterns environment
47 6,564,209 Knowledge management tool for providing abstracts of information
48 6,556,981 Fire detection systems and methods
49 6,556,659 Service level management in a hybrid network architecture
50 6,550,057 Piecemeal retrieval in an information services patterns environment

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Refine Search (multiprocessor or processors) and (modem or phone)

	PAT. NO.	Title
51	6,549,949	Fixed format stream in a communication services patterns environment
52	6,542,593	Rules database server in a hybrid communication system architecture
53	6,539,396	Multi-object identifier system and method for information service pattern environment
54	6,536,037	Identification of redundancies and omissions among components of a web based architecture
55	6,529,948	Multi-object fetch component
56	6,529,909	Method for translating an object attribute converter in an information services patterns environment
57	6,523,027	Interfacing servers in a Java based e-commerce architecture
58	6,519,592	Method for using data from a data query cache
59	6,519,571	Dynamic customer profile management
60	6,510,468	Adaptively transforming data from a first computer program for use in a second computer program
61	6,502,213	System, method, and article of manufacture for a polymorphic exception handler in environment services patterns
62	6,501,423	Method and system of directing an antenna in a two-way satellite system
63	6,496,850	Clean-up of orphaned server contexts
64	6,496,843	Generic object for rapid integration of data changes
65	6,493,721	Techniques for performing incremental data updates
66	6,484,161	Method and system for performing online data queries in a distributed computer system
67	6,482,156	Computerized medical diagnostic and treatment advice system including network access
68	6,477,665	System, method, and article of manufacture for environment services patterns in a netcentic

environment

69 6,477,580 Self-described stream in a communication services patterns environment

70 6,473,794 System for establishing plan to test components of web based framework by displaying pictorial representation and conveying indicia coded components of existing network framework

71 6,449,588 Customer-driven QOS in hybrid communication system

72 6,442,748 System, method and article of manufacture for a persistent state and persistent object separator in an information services patterns environment

73 6,442,547 System, method and article of manufacture for information service management in a hybrid communication system

74 6,441,782 Method and system of directing an antenna in a two-way satellite system

75 6,438,594 Delivering service to a client via a locally addressable interface

76 6,434,628 Common interface for handling exception interface name with additional prefix and suffix for handling exceptions in environment services patterns

77 6,434,568 Information services patterns in a netcentric environment

78 6,427,140 Systems and methods for secure transaction management and electronic rights protection

79 6,427,132 System, method and article of manufacture for demonstrating E-commerce capabilities via a simulation on a network

80 6,426,948 Video conferencing fault management in a hybrid network

81 6,421,683 Method and product for performing data transfer in a computer system

82 6,408,294 Common term optimization

83 6,405,364 Building techniques in a development architecture framework

84 6,405,195 System and method for collaborative hosted analysis of data bases via a network portal

85 6,397,228 Data enhancement techniques

86 6,393,415 Adaptive partitioning techniques in performing query requests and request routing

87 6,389,402 Systems and methods for secure transaction management and electronic rights protection

88 6,385,312 Automatic routing and information system for telephonic services

89 6,374,241 Data merging techniques

90 6,370,573 System, method and article of manufacture for managing an environment of a development architecture framework

91 6,363,488 Systems and methods for secure transaction management and electronic rights protection

92 6,362,779 Method and system for providing navigation systems with updated geographic data

93 6,345,288 Computer-based communication system and method using metadata defining a control-structure

94 6,345,239 Remote demonstration of business capabilities in an e-commerce environment

95 6,339,832 Exception response table in environment services patterns

96 6,335,927 System and method for providing requested quality of service in a hybrid network

97 6,332,163 Method for providing communication services over a computer network system

98 6,324,647 System, method and article of manufacture for security management in a development architecture framework

99 6,289,382 System, method and article of manufacture for a globally addressable interface in a communication services patterns environment

100 6,289,331 Fire detection systems using artificial intelligence

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Refine Search (multiprocessor or processors) and (modem or phone)

	PAT. NO.	Title
101	6,262,741	Tiling of object-based geographic information system (GIS)
102	6,256,773	System, method and article of manufacture for configuration management in a development architecture framework
103	6,253,193	Systems and methods for the secure transaction management and electronic rights protection
104	6,247,019	Object-based geographic information system (GIS)
105	6,237,786	Systems and methods for secure transaction management and electronic rights protection
106	6,233,571	Method and apparatus for indexing, searching and displaying data
107	6,206,829	Computerized medical diagnostic and treatment advice system including network access
108	6,199,082	Method for delivering separate design and content in a multimedia publishing system
109	6,195,697	System, method and article of manufacture for providing a customer interface in a hybrid network
110	6,188,751	Call processing system with call screening
111	6,185,576	Defining a uniform subject classification system incorporating document management/records retention functions
112	6,147,975	System, method and article of manufacture of a proactive threhold manager in a hybrid communication system architecture
113	6,121,924	Method and system for providing navigation systems with updated geographic data
114	6,113,540	Computerized medical diagnostic and treatment advice system
115	6,091,810	Automatic routing and information system for telephonic services
116	6,088,717	Computer-based communication system and method using metadata defining a control-structure

117 6,081,518 System, method and article of manufacture for cross-location registration in a communication system architecture
118 6,071,236 Method of determining mental health status in a computerized medical diagnostic system
119 6,055,513 Methods and apparatus for intelligent selection of goods and services in telephonic and electronic commerce
120 6,026,381 Financial market classification system
121 6,022,315 Computerized medical diagnostic and treatment advice system including network access
122 5,999,525 Method for video telephony over a hybrid network
123 5,982,891 Systems and methods for secure transaction management and electronic rights protection
124 5,982,868 Automatic routing and information system for telephonic services
125 5,956,397 Automatic routing and information system for telephonic services
126 5,949,876 Systems and methods for secure transaction management and electronic rights protection
127 5,937,037 Communications system for delivering promotional messages
128 5,917,912 System and methods for secure transaction management and electronic rights protection
129 5,917,902 Methods and systems for determining the classification of a call
130 5,915,019 Systems and methods for secure transaction management and electronic rights protection
131 5,910,987 Systems and methods for secure transaction management and electronic rights protection
132 5,910,982 Automatic routing and information system for telephonic services
133 5,910,107 Computerized medical diagnostic and treatment advice method
134 5,907,608 Automatic routing and information system for telephonic services
135 5,905,791 Method and apparatus for providing combined communication services including local service and toll service
136 5,901,246 Ergonomic man-machine interface incorporating adaptive pattern recognition based control system
137 5,892,900 Systems and methods for secure transaction management and electronic rights protection
138 5,868,669 Computerized medical diagnostic and treatment advice system
139 5,867,821 Method and apparatus for electronically accessing and distributing personal health care information and services in hospitals and homes
140 5,867,562 Call processing system with call screening
141 5,867,495 System, method and article of manufacture for communications utilizing calling, plans in a hybrid network
142 5,867,494 System, method and article of manufacture with integrated video conferencing billing in a communication system architecture
143 5,862,325 Computer-based communication system and method using metadata defining a control structure
144 5,862,223 Method and apparatus for a cryptographically-assisted commercial network system designed to facilitate and support expert-based commerce
145 5,848,373 Computer aided map location system
146 5,848,131 Automatic information and routing system for telephonic services
147 5,832,494 Method and apparatus for indexing, searching and displaying data
148 5,832,187 Fire detection systems and methods
149 5,724,968 Computerized medical diagnostic system including meta function
150 5,711,297 Computerized medical advice system and method including meta function

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Refine Search (multiprocessor or processors) and (modem or phone)

	PAT. NO.	Title
151	5,660,176	Computerized medical diagnostic and treatment advice system
152	5,594,638	Computerized medical diagnostic system including re-enter function and sensitivity factors
153	5,557,260	System for the monitoring and detection of heat sources in open areas
154	5,544,352	Method and apparatus for indexing, searching and displaying data
155	5,519,618	Airport surface safety logic
156	5,374,932	Airport surface surveillance system
157	5,140,419	Communications system
158	4,739,398	Method, apparatus and system for recognizing broadcast segments

USPTO PATENT FULL-TEXT AND IMAGE DATABASE

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	PAT. NO.	Title
1	6,768,944	Method and system for controlling a vehicle
2	6,760,916	Method, system and computer program product for producing and distributing enhanced media downstreams
3	6,757,710	Object-based on-line transaction infrastructure
4	6,754,885	Methods and apparatus for controlling object appearance in a process control configuration system
5	6,754,181	System and method for a directory service supporting a hybrid communication system architecture
6	6,748,353	Authoring language translator
7	6,742,015	Base services patterns in a netcentric environment
8	6,731,625	System, method and article of manufacture for a call back architecture in a hybrid network with support for internet telephony
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10	6,725,209	Computerized medical diagnostic and treatment advice system and method including mental status examination
11	6,721,726	Knowledge management tool
12	6,721,713	Business alliance identification in a web architecture framework
13	6,720,920	Method and arrangement for communicating between vehicles
14	6,718,535	System, method and article of manufacture for an activity framework design in an e-commerce based environment
15	6,715,145	Processing pipeline in a base services pattern environment
16	6,707,812	System, method and article of manufacture for element management in a hybrid communication system
17	6,704,873	Secure gateway interconnection in an e-commerce based environment

18 6,704,303 IP/telephony user interface for a hybrid communication system
19 6,701,345 Providing a notification when a plurality of users are altering similar data in a health care solution environment
20 6,671,818 Problem isolation through translating and filtering events into a standard object format in a network based supply chain
21 6,662,357 Managing information in an integrated development architecture framework
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